

CIC-56TS-40D-D6-YAM-S (REV 2)

56-Lead TSOP to a 40-Pin DIP

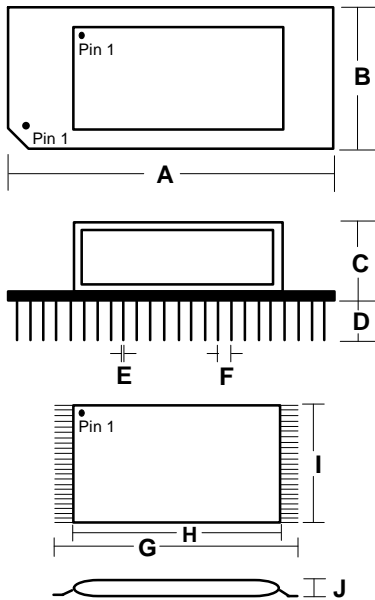
Mar-98

TSOP DEVICES: (EXAMPLES)	
E28F016XS ^A	E28F320J5
TE28F160S3 / S5 ^B	
(TE extended temp range)	

SOCKET: Yamaichi IC191-0562-003N

<u>PRICING:</u>	<u>QTY</u>	<u>PRICE EACH</u>
	1-4	\$190.00
	5-9	\$171.00
	10-24	\$161.50
	25-49	\$142.50

TYPICAL DIMENSIONS: (Drawings not to scale)



DIMENSION	MM	INCH
A	62.23	2.450
B	25.40	1.000
C	22.86	0.900
D Pin Length	10.16	0.400
E Pin Diameter	0.64	0.025
F Pin Spacing	2.54	0.100
ROW SPACING	15.24	0.600
G	20.00	0.787
H	18.40	0.724
I	14.00	0.551
J	1.20	0.047
PITCH	0.50	0.020

PIN CONFIGURATION:

BASE 40-Pin DIP	PIN NAMES	SOCKET 56-Lead TSOP
1	V _{PP}	15
2	3/5# ^A / NC ^B	1
3	NC	3
4	ADV# ^A / NC ^B	30
5	CLK ^A / NC ^B	29
6	CE ₁ #, CE ₀ # ¹	2, 14
7	A ₂₀	4
8	A ₁₉	5
9	A ₁₈	6
10	A ₁₇	7
11	GND, GND _Q (Planes 2 & 5)	21, 42, 48
12	DQ ₇	51
13	DQ ₆	49
14	DQ ₅	46
15	DQ ₄	44
16	DQ ₃	40
17	DQ ₂	38
18	DQ ₁	35
19	DQ ₀	33
20	OE#	54
21	A ₀	32
22	A ₁	28
23	A ₂	27
24	A ₃	26
25	A ₄	25
26	A ₅	24
27	A ₆	23
28	A ₇	22
29	A ₈	20
30	GND, GND _Q (Planes 2 & 5)	21, 42, 48
31	A ₉	19
32	A ₁₀	18
33	A ₁₁	17
34	A ₁₂	13
35	A ₁₃	12
36	A ₁₄	11
37	A ₁₅	10
38	A ₁₆	8
39	WE#	55
40	V _{CC} , V _{CCQ} (Planes 3 & 4)	9, 37, 43
V _{CCQ} Planes	RP#	16
V _{CCQ} Planes	WP#	56
GND _Q Planes	BYTE#	31

NOTES:

1. CE₁# (pin 2) and CE₀# (pin 14) are tied together on PC board.
- RY/BY#^A / STS^B (pin 53), DQ₁₅ - DQ₈ (pins 52, 50, 47, 45, 41, 39, 36 & 34) are not connected.
- Bypass capacitors (Type 1206) may be added between V_{CCQ} planes & GND_Q planes; as required by Programmer Vendor.

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